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2123
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Application of:

Gary L. Swoboda, et al.

TI-30480

Art Unit: 2125

Serial No: 09/943,598 ✓

Examiner:

Filed: August 30, 2001

Conf. No.: 2621


For: Synchronizing On-Chip Data Processor Trace and Timing Information for Export

INFORMATION DISCLOSURE STATEMENT

Ass't Commissioner for Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Ass't Commissioner for Patents, Washington, D.C. 20231 on August 21, 2002.


Robin E. Barnum

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

Since this IDS is being submitted before any Office Action, no fee is due.

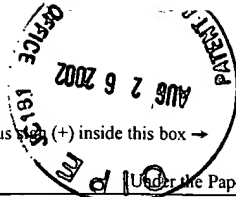
Respectfully submitted,



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PTO/SB/08A (08-00)

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				Application Number	
				Filing Date	August 30, 2001
				First Named Inventor	Gary L. Swoboda, et al.
				Group Art Unit	2123
				Examiner Name	
Sheet	1	of	1	Attorney Docket No.	TI-30480

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	ARM Limited, RDI 1.5.1tx and RDI 1.5.1rt; Doc. No. RDI-0032-CUST-ESPC-A; 19 May, 2000; pgs. 1-55.	
	CB	ARM Limited, ETM9, Rev. 1, Technical Reference Manual, Doc. No. DDI 0157C, pgs. i – Index-3.	
	CC	ARM Limited, Embedded Trace Macrocell, Rev. 1, Specification, Doc. No. IHI 0014E, pgs. i – Index-3.	
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